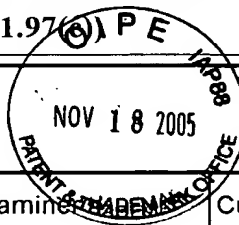


TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**(Under 37 CFR 1.97(b) or 1.97(c))**

Docket No.

200-0664

In Re Application Of: **Joseph G. Walacavage et al.**

Application No.

09/965,905

Filing Date

September 28, 2001

Examiner

J. Proctor

Customer No.

33481

Group Art Unit

2123

Confirmation No.

4248

Title: **METHOD OF PART FLOW MODEL FOR PROGRAMMABLE LOGIC CONTROLLER LOGICAL VERIFICATION SYSTEM**

Address to:

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

37 CFR 1.97(b)

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☐ the statement specified in 37 CFR 1.97(e);**OR**☐ the fee set forth in 37 CFR 1.17(p).

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
200-0664

In Re Application: **Joseph G. Walacavage et al.**

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| Application No. 09/965,905 | Filing Date September 28, 2001 | Examiner J. Proctor | Customer No. 33481 | Group Art Unit 2123 | Confirmation No. 4248 |
|--------------------------------------|--|-------------------------------|------------------------------|-------------------------------|---------------------------------|

Title: **METHOD OF PART FLOW MODEL FOR PROGRAMMABLE LOGIC CONTROLLER LOGICAL VERIFICATION SYSTEM**

Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
- ☐ The Director is hereby authorized to charge and credit Deposit Account No. **06-1510** as described below.
- ☐ Charge the amount of _____
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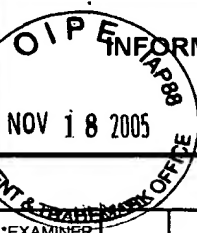
***This certificate may only be used if paying by deposit account.**

Dated: **November 18, 2005**

Daniel H. Bliss (Reg. No. 32,398) [0693.00242]
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Record I.D. 81082348

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INFORMATION DISCLOSURE CITATION

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200-0664

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09/965,905

APPLICANT(S)

J. G. Walacavage et al.

FILING DATE

September 28, 2001

GROUP

2123

U.S. PATENT DOCUMENTS

| *EXAMINER INITIAL | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
|----------------------|-----------------|------------|-------------------------|-------|----------|-------------------------------|
| | 6,442,441 | 08/27/2002 | J. G. Walacavage et al. | | | |
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FOREIGN PATENT DOCUMENTS

| | | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION | |
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.